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Conf. No.: 1115

JAN 10 2005

Serial No.: 10/604,116

Art Unit: 2823

Filed: 06/26/2003

Dkt. #: BUR920030031US1 (JBMB-0028)

Title: METHOD OF FORMING
FREESTANDING
SEMICONDUCTOR LAYER

Examiner: Garcia, Joannie A

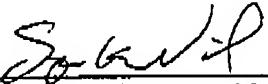
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DESTINATION FACSIMILE NUMBER: 703-872-9306

Transmitted herewith is: Response to Restriction Requirement in 3 pages

in the above identified application.

- No additional fee is required.
- The Commissioner is hereby authorized to charge and credit Deposit Account No. 09-0456 (JBM) as described below.
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Spencer K. Warnick, Reg. No. 40,398

DATE: January 10, 2005

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FREESTANDING
SEMICONDUCTOR LAYER**Examiner:** Garcia, Joannie AMail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

This paper is being filed in response to the Restriction Requirement dated December 21, 2004. Applicants hereby provisionally elect Group I, claims 1-10 and 16-20, with traverse.

REMARKS

Claims 1-20 are pending in the application. By this Response, claims 1-11 and 16-20 have been elected for examination. Prompt examination on the merits is respectfully requested.

Applicants first note that in the Restriction Requirement, the Office erroneously identified claims 165-20 as part of group I. Applicants assume it is a typographical error, however clarification is respectfully requested.

10/604,116